

















Datasheet

Distec

DD-0700!AE11

88!\$%1005

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PRODUCT SPECIFICATIONS(Preliminary)

	No					
No.: E	Customer Model No.			VAL FOR SAMPLE		
	D-0700-ME11		Date : 2	2021-03-03		
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2. Revision Record

Date	Rev.No.	Page	Revision Items	Prepared
2021.03.03	V0		The first release	LC
2021.12.21	V1		Updated UL No. in Item #3	CS





3. General Specifications

DD-0700-ME11 is a TFT-LCD module. It is composed of a TFT-LCD panel, driver IC, FPC, a back light and PCB unit . The 7.0" display area contains 800 x (RGB) x 480 pixels and can display up to 16.7M colors. This product is RoHS compliant. UL No. 62368-1

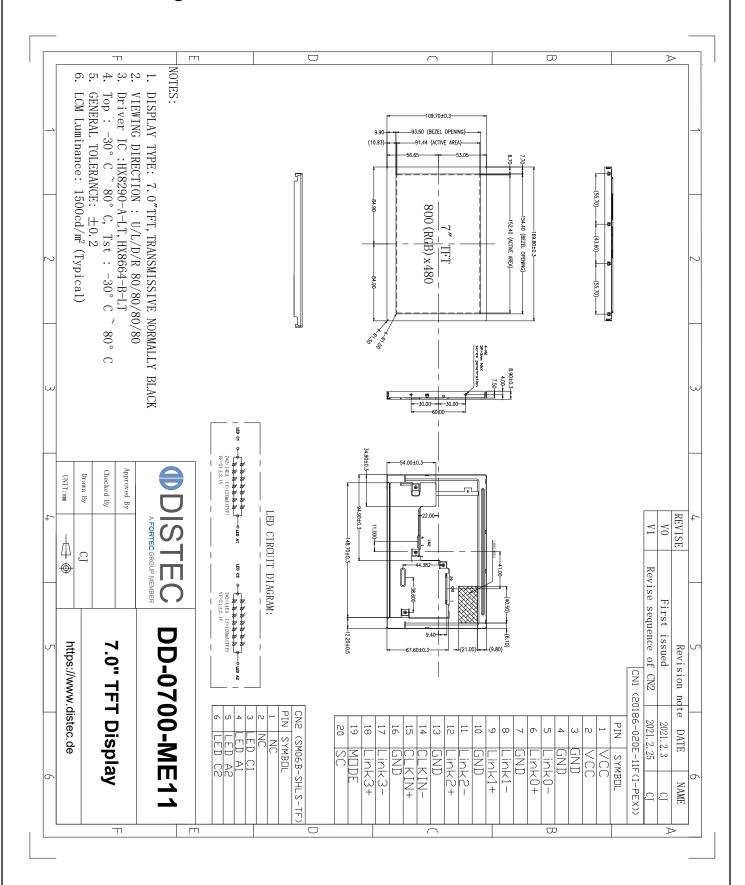
Item	Contents	Unit	Note
LCD Type	TFT	-	
Display color	16.7M		1
Viewing Direction	ALL	O'Clock	
Display Mode	Transmissive,Normally Black		
Operating temperature	-30 ~ +80	$^{\circ}\!\mathbb{C}$	
Storage temperature	-30 ~ +80	$^{\circ}$	
Module size	169.80 x 109.70 x 8.90	mm	2
Active Area(W×H)	152.40 x 91.44	mm	
Number of Dots	800 x 480	dots	
TFT Driver IC	HX8290-A-LT & HX8664-B-LT	-	
Power Supply Voltage	3.3	V	
Weight	-	g	
Interface	LVDS	-	
Surface treatment	Anti-Glare and hard-coating 3H		

Note 1: Color tune is slightly changed by temperature and driving voltage.

Note 2: Without FPC and Solder.With PCB.



4. Outline. Drawing





5. Absolute Maximum Ratings(Ta=25 \mathcal{C})

5.1 Electrical Absolute Maximum Ratings.(Vss=0V, Ta=25 \mathcal{C})

Item	Symbol	Min.	Max.	Unit	Note
Power Supply Voltage	VCC	-0.3	4.0	V	1, 2

Notes:

- 1. If the module is above these absolute maximum ratings. It may become permanently damaged. Using the module within the following electrical characteristic conditions are also exceeded, the module will malfunction and cause poor reliability.
- 2. V_{CC} >V_{SS} must be maintained.
- 3. Please be sure users are grounded when handing LCD Module.

5.2 Environmental Absolute Maximum Ratings.

Item	Stor	age	Operat	ing	Note
	MIN.	MAX.	MIN.	MAX.	
Ambient Temperature	-30℃	80℃	-30℃	80℃	1,2
Humidity	-	-	-	-	3

Notes:

- 1. The response time will become lower when operated at low temperature.
- 2. Background color changes slightly depending on ambient temperature.

 The phenomenon is reversible.

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3. Ta<=40 ℃:85%RH MAX.

Ta>=40 C:Absolute humidity must be lower than the humidity of 85%RH at 40 C.



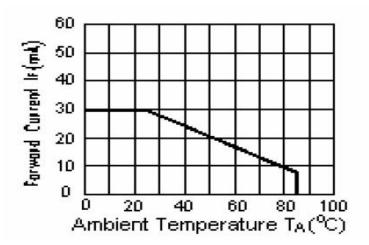
6. Electrical Specifications

6.1 Electrical characteristics(Vss=0V ,Ta=25 \mathcal{C})

Paramet	ter	Symbol	Condition	Min	Тур	Max	Unit	Note
Power su	pply	VCC	Ta=25℃	3.0	3.3	3.6	V	
Input	'H'	ViH	Ta=25℃	0.7VCC	-	VCC	V	
voltage 'L'	'L'	VıL	Ta=25℃	-0.3	-	0.3VCC	\ \	
Current power sup		ICC	Ta=25℃	-	100	-	mA	

6.2 LED backlight specification(VSS=0V ,Ta=25 \mathcal{C})

Item	Symbol	Condition	Min	Тур	Max	Unit	Note
Supply voltage	Vf	lf=240mA	18.9	21.0	23.1	V	
Uniformity	ΔВр	lf=240mA	75	80	-	%	
Life Time	time	If=240mA	20K	50K	-	hours	1



Note 1: Brightness to be decreased to 50% of the initial value at ambient temperature TA=25 $^{\circ}$ C



6.3 Interface signals

6.3.1CN 1(Interface Signal)

Pin No.	Symbol	I/O	Function			
1-2	VCC	Р	Power supply			
3-4	GND	Р	Ground.			
5	LINK0-	I	LVDS lane0 input	LVDC long Cinnert		
6	LINK0+	I	EVD3 laried input			
7	GND	Р	Ground.			
8	LINK1-	I	IV/DC land1 input			
9	LINK1+	I	LVDS lane1 input			
10	GND	Р	Ground.			
11	LINK2-	I	LV/DO L O: I			
12	LINK2+	I	LVDS lane2 input			
13	GND	Р	Ground.			
14	CLKIN-	I	LV/DC CLIZ input			
15	CLKIN+	I	LVDS CLK input			
16	GND	Р	Ground.			
17	LINK3-	I	IV/DC long? innut			
18	LINK3+	I	LVDS lane3 input			
19	MODE		MODE=1	LVDS 8 BIT		
19	INIODE	'	MODE=0	LVDS 6 BIT		
20	SC	I	Scan direction control (Low=Normal, High=Reverse)			

6.3.2 CN 2(Backlight)

Pin No.	Symbol	I/O	Function
1-2	NC	-	No connection
3	LEDC1	Р	LED back light(Cathode)
4	LEDA1	Р	LED back light(Anode)
5	LEDA2	Р	LED back light(Anode)
6	LEDC2	Р	LED back light(Cathode)

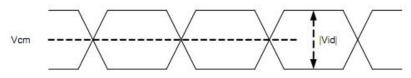


6.4 AC Characteristics

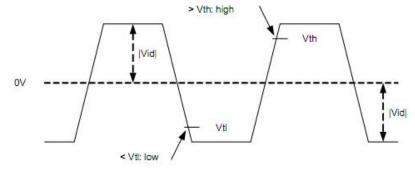
6.4.1 For the digital circuit: LVDS mode

Donous de la	Comples	Condition		Spec.				
Parameter	Symbol	Condition	Min.	Тур.	Max.	Unit		
Differential input high Threshold voltage	Vth	Vcm=1.2V	-	-	+0.1	V		
Differential input low threshold voltage	Vtl	æ	-0.1	-	-	V		
Differential input common Mode voltage	V _{CM}	•	1	1.2	1.7- V _{id} /2	V		
LVDS input voltage	V _{INLV}	-	0.7		1.7	V		
Differential input voltage	Vid		0.1	12	0.6	V		
Differential input leakage Current	Ilvleak		-10		+10	μA		

Single-ended: LVCLKP(R), LVCLKN(R), LVD[3:0]P(R), LVD[3:0]N(R)



Differential: LVCLKP(R)-LVCLKN(R), LVD [3:0]P(R)-LVD [3:0]N(R)



6.4.2 For the analog circuit: Normal mode

D	0	0		Unit		
Parameter	Symbol	Conditions	Min.	Typ.	Max.	Unit
Analog positive supply voltage	VSP	VSP is generated by PFM, VSPS [4:0]=14h, with proper settings and components.	6.7	7	7.3	V
Analog negative supply voltage	VSN	VSN is generated by PFM, VSNS [4:0]=14h, with proper settings and components.	-7.3	-7	-6.7	V
Source driver positive supply voltage	VSDP	VSP≧7V,VSDPS[4:0]=14h, loading current=0	6.65	6.8	6.95	V
Source driver negative supply voltage	VSDN	VSN=-7V, VSDNS[4:0]=14h, loading current=0	-6.95	-6.8	-6.65	V
Output for positive gamma reference high voltage	VGMPHO	VSDP≧6.8V, VGMPHS[4:0]=0x1Ah	6.48	6.6	6.72	V
Output for positive gamma reference voltage	VGMPMO	VSDP≧6.8V, VGMPHS[4:0]=0x1Ah VGMPLS[3:0]=0x00h	3.3	3.4	3.5	V



-	-		_		_	
Output for positive gamma reference low voltage	VGMPLO	VGMPLS[3:0]=0x00h	0.12	0.2	0.28	V
Output for negative gamma reference high voltage	VGMNHO	VSDN≦-6.8V, VGMNHS[4:0]=0x1Ah	-6.72	-6.6	-6.48	V
Output for negative gamma reference voltage	VGMNMO	VSDN≦-6.8V, VGMNHS[4:0]=0x1Ah VGMNLS[4:0]=0x00h	-3.5	-3.4	-3.3	٧
Output for negative gamma reference low voltage	VGMNLO	VGMNLS[4:0]=0x00h	-0.28	-0.2	-0.12	V
VCOM voltage	VCOM	VCOMS[7:0]=0x80h	-1.53	-1.48	-1.43	V
Source output voltage, positive polarity	V _{SDOP}	-	0.2	<u>.</u>	VSDP-0.2	V
Source output voltage, negative polarity	V _{SDON}		VSDN+0.2	9	-0.2	V
Positive power supply	VGH	VGH is generated by charge pump, VGHS[3:0]=0x05h, loading current=0	14.6	15.6	16.6	V
Negative power supply	VGL	VGL is generated by charge pump, VGLS[2:0]=0x02h, loading current=0	-11	-10	-9	٧

B	0	0		Hait		
Parameter	Symbol	Conditions	Min.	Тур.	Max.	Unit
Source output voltage deviation		V _{SDOP} =0.5V to VSDP-0.5V, V _{SDON} =VSDN+0.5V to -0.5V	-	-	10	mV
	V _{OD}	V _{SDOP} =0.2V to 0.5V or V _{SDOP} =VSDP-0.5V to VSDP-0.2V, V _{SDON} =VSDN+0.2V to VSDN+0.5V or V _{SDON} =-0.5V to -0.2V	2	-	15	mV
Standby current (VCC1 + VCC2)	I _{STBvcc}	"STBYB=0" and all inputs are default.	-	12	100	μА
Standby current (VSN or VSP)	I _{STB}	"STBYB=0", VSP or VSN external input	Ē	*	100	μA



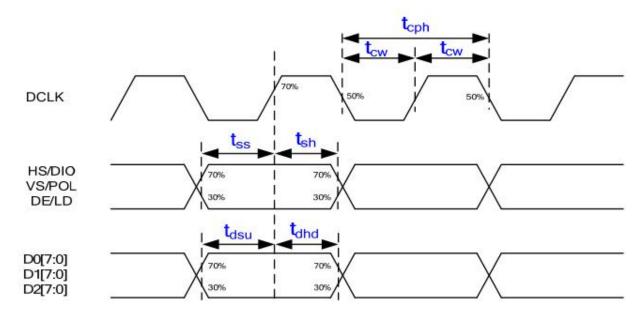
6.4.3 LVDS mode AC electrical characteristics

Parameter	Combal		Unit			
Parameter	Symbol	Min.	Тур.	Max.	Unit	
Clock frequency	F _{LVCYC}	20		85	MHz	
Clock period	T _{LVCYC}	11.76	-	111=	ns	
1 data bit time	UI	-	1/7	-	TLVCYC	
Clock high time	T _{LVCH}	2.8	4	4.2	UI	
Clock low time	T _{LVCL}	2.8	3	4.2	UI	
Position 1	T _{POS1}	-0.2	0	0.2	UI	
Position 0	T _{POS0}	8.0	1	1.2	UI	
Position 6	T _{POS6}	1.8	2	2.2	UI	
Position 5	T _{POS5}	2.8	3	3.2	UI	
Position 4	T _{POS4}	3.8	4	4.2	UI	
Position 3	T _{POS3}	4.8	5	5.2	UI	
Position 2	T _{POS2}	5.8	6	6.2	UI	
Input eye width	T _{EYEW}	0.6	2	1/2	UI	
Input eye border	T _{EX}	-		0.2	UI	
LVDS wake up time	T _{ENLVDS}	<u>-</u>	2	150	us	

LVDS with SSC

Barrantas	Compleal	Canadista		1144		
Parameter	Symbol	Condition	Min.	Тур.	Max	Unit
Modulation Frequency		LVDS clock frequency center at 80MHz	(=)	(=0)	200	KHz
	SSC	LVDS clock frequency center at 60MHz	(4)	(=)	150	KHz
	SSCMF	LVDS clock frequency center at 40MHz	(-)	-	100	KHz
		LVDS clock frequency center at 20MHz	-	-	50	KHz
Modulation Rate	SSCMR	LVDS clock frequency + SSCMR in the range of 20MHZ~85Mhz	(=)	-0	±5	%

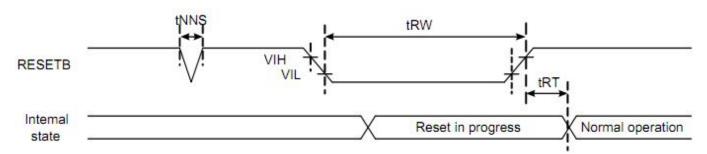
6.4.4 TTL mode AC electrical characteristics





Danamatan	Compleal		311.34			
Parameter	Symbol	Min.	Typ.	Max.	Unit	
DCLK period	T _{cph}	16.67	-	-	ns	
DCLK duty ratio	T _{CW}	40	50	60	%	
Data setup time	T _{dsu}	5	-	-	ns	
Data hold time	T _{dhd}	5	-	~	ns	
VS/POL setup time	T _{ss}	5	-	-	ns	
VS/POL hold time	T _{sh}	5	91	~	ns	
HS/DIO setup time	T _{ss}	5		-	ns	
HS/DIO hold time	T _{sh}	5	-	~	ns	
DE/LD setup time	T _{ss}	5	-	-	ns	
DE/LD hold time	T _{sh}	5	<u>u</u> 1	2	ns	

6.5 Reset timing



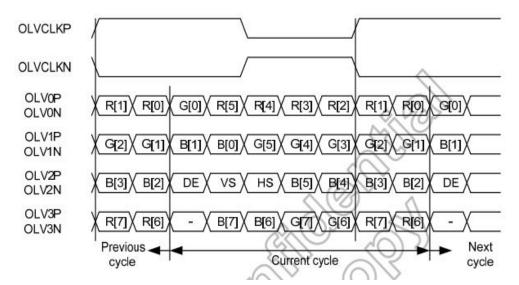
Cianal	Signal Bosomator			Unit			
Signal	Parameter	Symbol	Min.	Тур.	Max.	Unit	
	Reset pulse width	tRW	10	-	-	μs	
RESETB	Reset complete time	tRT	-	-	5	μs	
1 1111 W	Negative spike noise width	tNNS	,	2	100	ns	

6.6 Input timing table

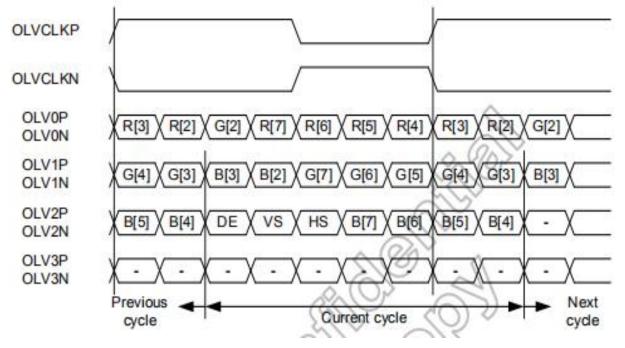
· · · · · · · · · · · · · · · · · · ·	ITEM		SYMBOL	MIN	TYP	MAX	UNIT	
	Frequency		fclk	25	30.4	45	MHz	
DCLK	Period		tclk	22.2	32.9	40	ns	
Hor		Active Time	thA	800	800	800	tclk	
	Horizontal	Blanking Time	t _{HB}	20	160		$t_{\rm CLK}$	
	Horizontai	Frequency	fH	26.4	31.7	45	kHz	
		Period	tH	22.2	31.6	37.9	μs	
DENA		Active Time	tvA	480	480	480	tн	
	Vertical	Blanking Time	tvB	3	48		$t_{ m H}$	
	vertical	Frequency	f_V	55	60	75	$_{\mathrm{Hz}}$	
		Period	tv	13.3	16.7	18.2	ms	



6.7 LVDS interface



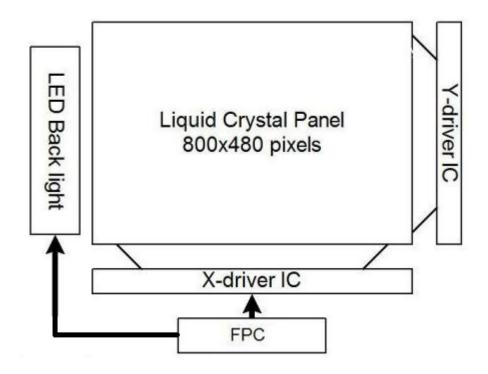
8bit mode



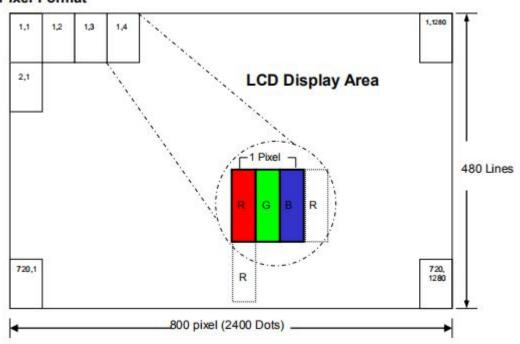
6bit mode



6.8 Block Diagram



Pixel Format







Relationship Between Displayed Color and Input

		MS	3B					L	38	MS	38					L	SB	MS	зВ					L	SB	Gray scale
	Display	RT	Res	R5	84	R3 I	62 1	RS	RO	GF.	G6	05	64	63	62	01	co	87	86	85	84	83	82	81	80	Level
	Black	L	L	L	L	L		L	E	L	L	L	L	L	L	L	L	L	L	L	L	L	L	E	L	
	Blue	L	L	L	L	L		L	L	L	L	L	L	L	L	L	L	Н	Н	Н	Н	Н	Н	Н	H	
	Green	L	L	L	L	L	L	L	L	H	H	Н	Н	Н	H	Н	Н	L	L	L	L	L	L	L	L	2
Basic	Light Blue	L	L	L	L	L		L	L	H	н	Н	Н	Н	Н	Н	Н	Н	Н	Н	Н	Н	H	Н	Н	
color	Red	Н	Н	Н	Н	Н	H	H	H	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	i ii
	Purple	Н	Н	Н	Н	Н	Н	Н	H	L	L	L	L	L	L	L	L	Н	Н	Н	Н	Н	Н	Н	Н	
	Yellow	Н	Н	H	Н	H	Н	H	H	Н	H	Н	H	Н	Н	Н	Н	L	L	L	L	L	L	L	L	
	White	Н	Н	Н	Н	H	Н	Н	Н	H	H	H	Н	H	Н	H	Н	Н	Н	H	Н	Н	Н	Н	Н	
	Black	L	L	L	L	L		L	L	Ш	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L0
	- 12	L	L	L	L	L	L	L	H	4	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L1
	Dark	L	L	L	L	L		Н	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	E	L	L2
Gray scale	†				- 33				Ť												- 4					L3L251
of Red	4	Н	Н	Н	Н	н	Н	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L252
	Light	Н	Н	Н	Н	Н	Н	L	Н	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L253
	E1163.12	Н	Н	Н	Н	н	Н	Н	I	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L254
	Red	Н	Н	Н	Н	Н	Н	Н	Н	Ш	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	Red L255
	Black	L	L	L	L	L		L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L0
	W W	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	Н	L	L	L	L	L	L	L	L	L1
Gray scale	Dark	L	L	L	L	L		L	L	L	L	L	L	L	L	Н	L	L	L	L	L	L	L	L	L	L2
	†				13	:											T									L3L251
of Green	1	L	L	L	L	L		L	L	Н	н	Н	н	Н	Н	L	L	L	L	L	L	L	L	L	L	L252
	Light	L	L	L	L	L		L	L	Н	н	Н	Н	Н	Н	L	Н	L	L	L	L	L	L	L	L	L253
		L	L	L	L	L		L	E	Н	Н	Н	Н	Н	Н	Н	L	E	L	L	L	L	L	E	L	L254
	Green	L	L	L	L	L		L	Ĺ	H	н	Н	Н	н	Н	н	Н	L	L	L	L	L	L	L	L	Green L255
	Black	L	L	L	L	L		L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	LO
		L	L	L	L	L		L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	Н	L1
	Dark	L	L	L	L	L	L	L	L	Ĺ	L	L	L	L	L	L	L	L	L	L	L	L	L	Н	L	L2
Gray scale	†				35				Ţ												- 3					L3L251
of Blue	1	L	L	L	L	L		L	L		L	L	L	L	L	L	L	Н	Н	Н	Н	Н	Н	L	L	L252
	Light	L	ī	L	1	L		L	T	L		L	ī	L	L	L	L	Н	Н	Н	Н	Н	Н	L	н	1.253
				L	L	L		L	L	L											_	_	_	Н		L254
	Blue	L	L	L	L	L		L	L	L	L	L	L	L	L	L	L	Н	Н	Н	Н	Н	Н	Н	Н	Blue L255
	Black	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L0
		L	E	L	L	L		L	Н	L	L	L	L	L	L	L	Н	L	L	L	L	L	L	L	Н	L1
	Dark	L	L	L	L	L		Н	L	L	L	L	L	L	L	Н	L	L	L	L	L	L	L	Н	L	L2
Gray scale	Ť																									L3···L251
of White & Black	1	Н	н	Н	Н	н	Н	L	L	Н	н	Н	Н	н	Н	1	L	Н	Н	Н	Н	Н	Н	L	L	L252
DISTA	Light			-	Н					H				Н		ī				Н				ī		L253
				_	_	Н	-	_		_	_	_		_		Н				_	_	_	-	Н		L254
	White																1			_						White L255



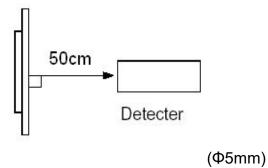
7. Optical Characteristics

Item	Sy	mbol	Condition	Min.	Тур.	Max.	Unit	Note	
Luminance	Вр		<i>θ</i> =0°	-	1500	-	Cd/m ²	1	
Uniformity	_	Вр	Ф=0°	75	80	-	%	1,2	
	3	:00		-	80	-			
Viewing	6	:00	0:540	-	80	-			
Angle	9	:00	Cr≥10	-	80	-	Deg 3	3	
	12	2:00		-	80	-			
Contrast Ratio	Cr		<i>θ</i> =0°	500	800	-	-	4	
Response Time	$T_{r+}T_{f}$		Ф=0°	-	25	35	ms	5	
	W	х			0.306		-		
	VV	у			0.331		-		
	R	х			0.648		-		
Color of CIE	K	у		Тур	0.317	Тур	-		
Coordinate		Х	<i>θ</i> =0° Φ=0°	-0.05	0.286	+0.05	-	1,6	
	G	у	Ψ-0		0.606		-		
	В	Х			0.138		-		
	D	у			0.098		-		
NTSC Ratio		S		-	71.7	-	%		

Note: The parameter is slightly changed by temperature, driving voltage and material



Note 1: The data are measured after LEDs are turned on for 5 minutes. LCM displays full white. The brightness is the average value of 9 measured spots. Measurement equipment BM-7



Measuring condition:

- Measuring surroundings: Dark room.
- Measuring temperature: Ta=25 \mathcal{C} .
- Adjust operating voltage to get optimum contrast at the center of the display.

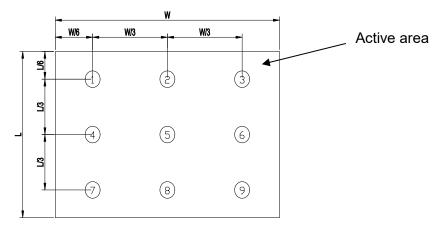
Measured value at the center point of LCD panel after more than 5 minutes while backlight turning on.

Note 2: The luminance uniformity is calculated by using following formula.

 $\triangle Bp = Bp (Min.) / Bp (Max.) \times 100 (%)$

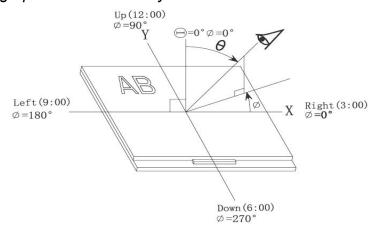
Bp (Max.) = Maximum brightness in 9 measured spots

Bp (Min.) = *Minimum brightness in 9 measured spots.*

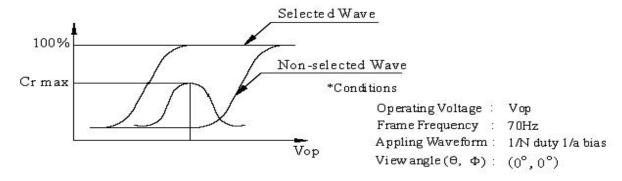




Note 3: The definition of viewing angle: Refer to the graph below marked by θ and Φ



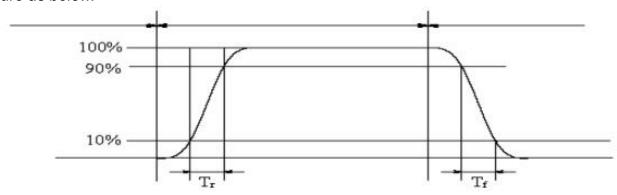
Note 4: Definition of contrast ratio.(Test LCD using DMS501)



$$Contrast \ ratio(Cr) = \frac{Brightness \ of \ selected \ dots}{Brightness \ of \ non-selected \ dots}$$

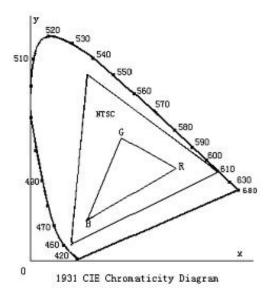
Note 5: Definition of Response time. (Test LCD using DMS501):

The output signals of photo detector are measured when the input signals are changed from "black" to "white" (falling time) and from "white" to "black" (rising time), respectively. The response time is defined as the time interval between the 10% and 90% of amplitudes. Refer to figure as below.



The definition of response time

Note 6: Definition of Color of CIE Coordinate and NTSC Ratio.

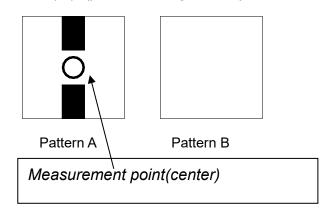


Color gamut:

 $S = \frac{area~of~RGB~triangle}{area~of~NTSC~triangle} \times 100\%$

Note 7: Definition of cross talk.

Cross talk ratio(%)=|pattern A Brightness-pattern B Brightness|/pattern A Brightness*100



Electric volume value=3F+/-3Hex





8. Reliability Test Items and Criteria

Test Item	Test condition	Remark
High Temperature Storage	Ta = 80℃ 96hrs	Note1,Note3, 4
Low Temperature Storage	Ta = -30℃ 96hrs	Note1,Note3, 4
High Temperature Operation	Ta = 70℃ 96hrs	Note2, Note3, 4
Low Temperature Operation	Ta = -30℃ 96hrs	Note1,Note3, 4
Operation at High Temperature/Humidity	+60℃, 90%RH 96hrs	Note3, 4
Thermal Shock	-30°C/30 min ~ +80°C/30 min for a total 10 cycles, Start with cold temperature and end with high temperature.	Note3, 4
Vibration Test	Frequency range:10~55Hz Stroke:1.5mm Sweep:10Hz~55Hz~10Hz 2 hours for each direction of X. Y. Z. (6 hours for total)	
Mechanical Shock	100G 6ms,±X, ±Y, ±Z 3 times for each direction	
Package Vibration Test	Random Vibration: 0.015G*G/Hz from 5-200HZ, -6dB/Octave from 200-500HZ 2 hours for each direction of X. Y. Z. (6 hours for total)	
Package Drop Test	Height:60cm 1 corner, 3 edges, 6 surfaces	
Electro Static Discharge	±2KV, Human Body Mode, 100pF/1500Ω	

Note 1: Ta is the ambient temperature of samples.

Note 2: Ts is the temperature of panel's surface.

Note 3: In the standard condition, there shall be no practical problem that may affect the display function. After the reliability test, the product only guarantees operation, but don't guarantee all of the cosmetic specification.

Note 4: Before cosmetic and function test, the product must have enough recovery time,at least 2 hours at room temperature



9. Precautions for Use of LCD Modules

9.1 Handling Precautions

- 9.1.1 The display panel is made of glass. Do not subject it to a mechanical shock by dropping it from a high place, etc.
- 9.1.2 If the display panel is damaged and the liquid crystal substance inside it leaks out, be sure not to get any in your mouth, if the substance comes into contact with your skin or clothes, promptly wash it off using soap and water.
- 9.1.3 Do not apply excessive force to the display surface or the adjoining areas since this may cause the color tone to vary.
- 9.1.4 The polarizer covering the display surface of the LCD module is soft and easily scratched. Handle this polarizer carefully.
- 9.1.5 If the display surface is contaminated, breathe on the surface and gently wipe it with a soft dry cloth. If still not completely clear, moisten cloth with one of the following solvents:

— Isopropyl alcohol	— Ethyl alcohol	
Solvents other than those m	entioned above may damage the polarizer.	Especially, do
not use the following:		

- Water Ketone Aromatic solvents
- 9.1.6 Do not attempt to disassemble the LCD Module.
- 9.1.7 If the logic circuit power is off, do not apply the input signals.
- 9.1.8 To prevent destruction of the elements by static electricity, be careful to maintain an optimum work environment.
 - a. Be sure to ground the body when handling the LCD Modules.
 - b. Tools required for assembly, such as soldering irons, must be properly ground.
 - c. To reduce the amount of static electricity generated, do not conduct assembly and other work under dry conditions.
 - d. The LCD Module is coated with a film to protect the display surface. Be care when peeling off this protective film since static electricity may be generated.



9.2 Storage precautions

- 9.2.1 When storing the LCD modules, avoid exposure to direct sunlight or to the light of fluorescent lamps.
- 9.2.2 The LCD modules should be stored under the storage temperature range. If the LCD modules will be stored for a long time, the recommend condition is:

Temperature : 0 $^{\circ}$ $^{\circ}$ $^{\circ}$ 40 $^{\circ}$

Relatively humidity: ≤ 80%

- 9.2.3 The LCD modules should be stored in the room without acid, alkali and harmful gas.
- 9.3 The LCD modules should be no falling and violent shocking during transportation, and also should avoid excessive press, water, damp and sunshine.

<u>END</u>



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